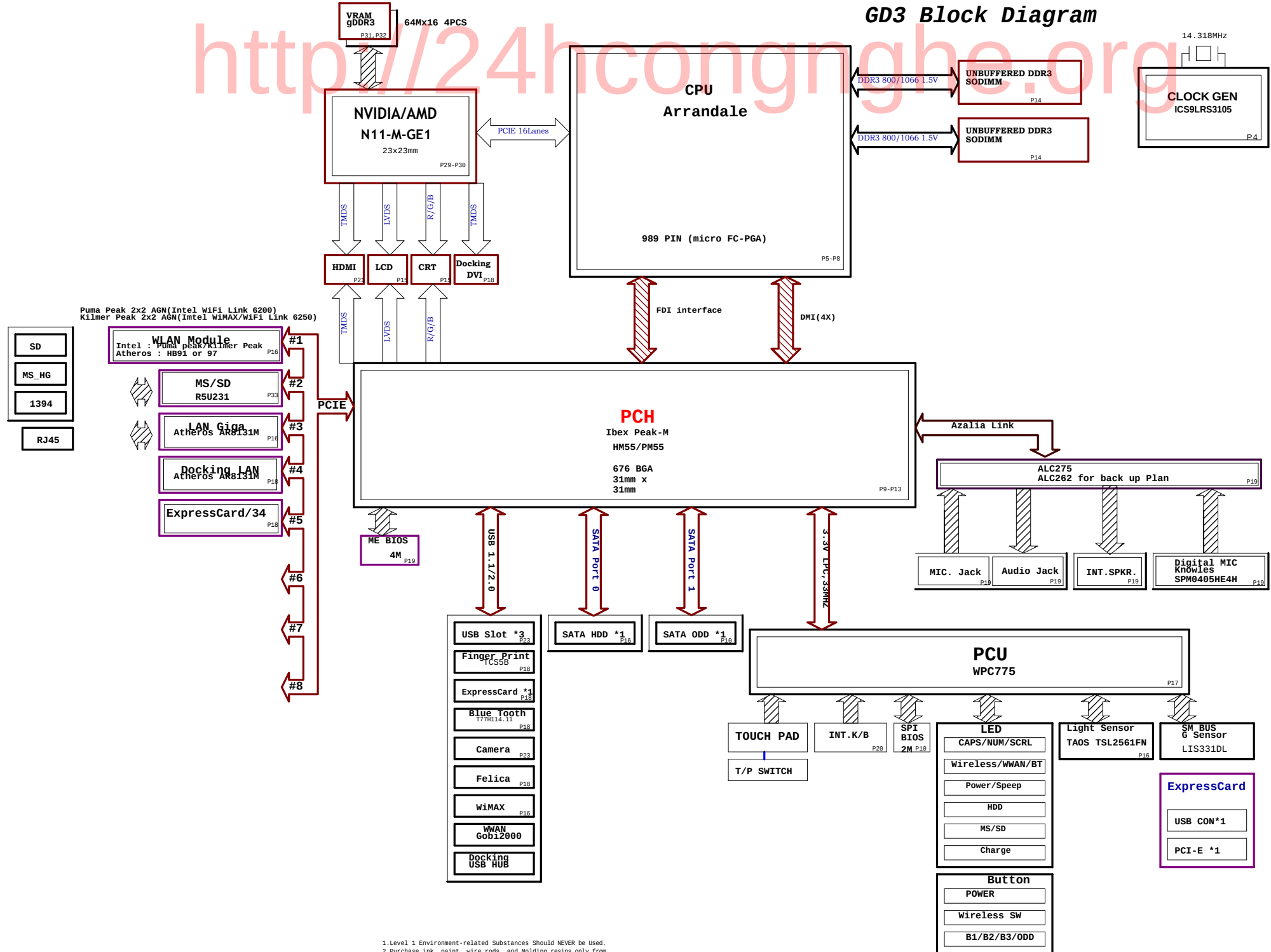


<http://24hcongnghe.org>

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02	Block Diagram	1A	
03	Change List	1A	
04	ICS9LR3197	1A	
05	PROCESSOR 1/4(DMI&HOST&PCI)	1A	
06	PROCESSOR 2/4(DDR)	1A	
07	PROCESSOR 3/4(POWER)	1A	
08	PROCESSOR 4/4(GND)	1A	
09	PCH 1/5 (DMI&VIDEO)	1A	
10	PCH 2/5 (SATA/LPC/Azalia)	1A	
11	PCH 3/5 (PCI/PCIE/CLK/USB)	1A	
12	PCH 4/5 (GPIO)	1A	
13	PCH 5/5 (POWER)	1A	
14	DDR3 DIMM	1A	
15	CRT/LVDS	1A	
16	WWAN/Light Sensor	1A	
17	WPCE775L & FLASH	1A	
18	Docking/35001/TP/S1	1A	
19	HDD/ODD/SSD	1A	
20	KB/USB/FAN/Reset	1A	
21	HDMI	1A	
22	POWER CPU CORE (ISL62882)	1A	
23	POWER 3VPCU&5VPCU(PM6686)	1A	
24	POWER 1.5VSUS/VTT_MEM	1A	
25	POWER VCC1.05(0Z8111LN)-26A	1A	
26	POWER VCC_CFXCORE(MAX17028)	1A	
27	POWER(ADAPTER IN / CONN)	1A	
28	POWER VCC1.8	1A	
29	NVIDIA N11M-GE1 PCIE&PW 1/3	1A	
30	NVIDIA N11M-GE1 TMDS&DAC 2/3	1A	
31	NVIDIA N11M-GE1 VRAM 3/4	1A	
32	NVIDIA N11M-GE1 VRAM 4/4	1A	
33	R5U231	1A	

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Title			
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GD3 Block Diagram

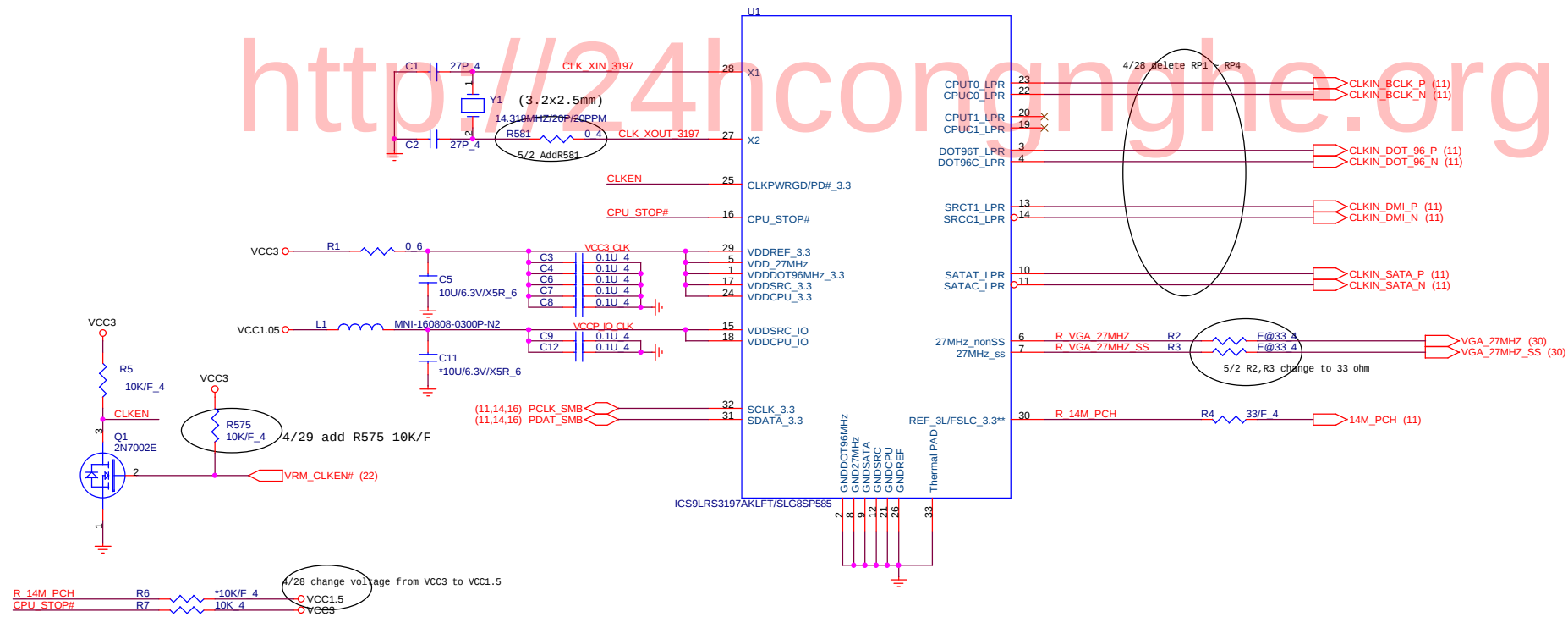


<http://24hcongnghe.org>

```
0501:
- P19 Delete Q27,R346,Q25,R344,Q26,Q24; Add U59
```

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CPU Frequency Select Table

FSLC	CPU MHz	SRC MHz	REF MHz	USB MHz	DOT MHz
0 (default)	133.33	100.00	14.318	48.00	96.00
1	100.00				

Table 2: pin 6, 7 Configuration

B1b4	B1b3	B1b2	B1b1	Pin 6 MHz	Pin 7 MHz	Spread %	Comment
0	0	0	0	N/A	N/A	N/A	N/A
0	0	0	1	N/A	N/A	N/A	N/A
0	0	1	0	27MHz_nonSS	27MHz_SS	-0.5%	
0	0	1	1	27MHz_nonSS	27MHz_SS	-1%	
0	1	0	0	27MHz_nonSS	27MHz_SS	-1.5%	
0	1	0	1	27MHz_nonSS	27MHz_SS	-2%	
0	1	1	0	27MHz_nonSS	27MHz_SS	-0.75%	
0	1	1	1	27MHz_nonSS	27MHz_SS	-1.25%	
1	0	0	0	27MHz_nonSS	27MHz_SS	-1.75%	
1	0	0	1	27MHz_nonSS	27MHz_SS	+0.5%	
1	0	1	0	27MHz_nonSS	27MHz_SS	+0.75%	
1	0	1	1	N/A	N/A	N/A	N/A
1	1	0	0	N/A	N/A	N/A	N/A
1	1	0	1	N/A	N/A	N/A	N/A
1	1	1	0	N/A	N/A	N/A	N/A
1	1	1	1	N/A	N/A	N/A	N/A

Table 4: Device ID table

B8b7	B8b6	B8b5	B8b4	Comment
0	0	0	0	56 pin TSSOP
0	0	0	1	64 pin TSSOP
0	0	1	0	Reserved
0	0	1	1	Reserved
0	1	0	0	Reserved
0	1	0	1	72 pin QFN
0	1	1	0	Reserved
0	1	1	1	Reserved
1	0	0	0	32 pin QFN
1	0	0	1	Reserved
1	0	1	0	Reserved
1	0	1	1	Reserved
1	1	0	0	Reserved
1	1	0	1	Reserved
1	1	1	0	Reserved
1	1	1	1	Reserved

Table 3: IO Vout select table

B9b2	B9b1	B9b0	IO_Vout
0	0	0	0.3V
0	0	1	0.4V
0	1	0	0.5V
0	1	1	0.6V
1	0	0	0.7V
1	0	1	0.8V
1	1	0	0.9V
1	1	1	1.0V

QUANTA COMPUTER

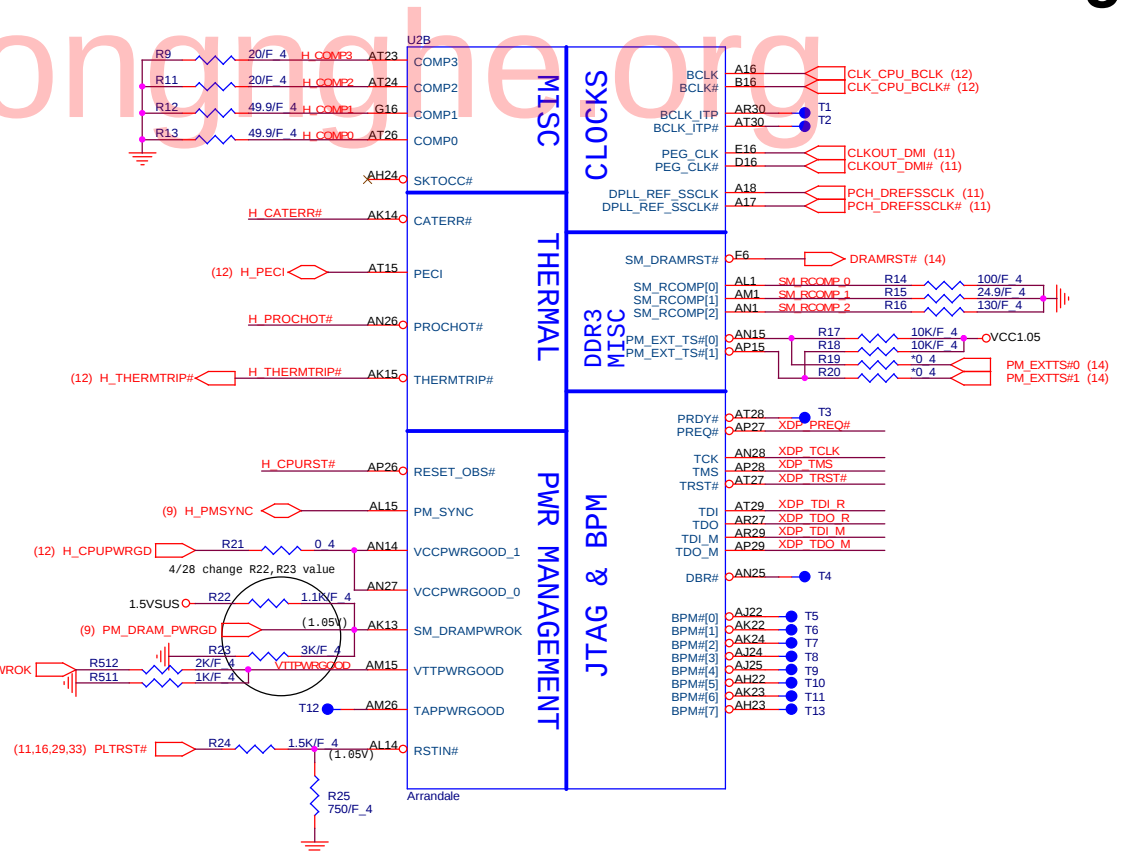
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Size: Document Number: **GD3 Main Board** Rev: 1A

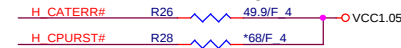
Date: Sunday, May 31, 2009 Sheet: 4 of 33

1. Level 1 Environment-related Substances Should NEVER be Used.

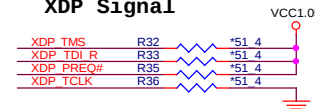
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



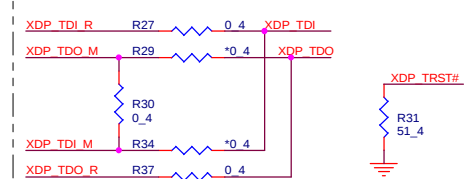
Processor Pullups



XDP Signal



JTAG MAPPING



Scan Chain (Default)	STUFF -> R88, R89, R91 NO STUFF -> R90, R92
CPU Only	STUFF -> R88, R90 NO STUFF -> R89,
GMCH Only	R92, R91 STUFF -> R92, R91 NO STUFF -> R88, R90, R89



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PROCESSOR 1/4(HOST&PCI)

Size	Document Number	Rev
Custom	CD3 Main Board	1A

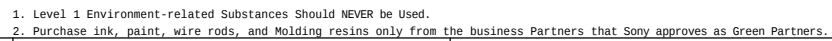
Customer: **GD3 Main Board** Date: Thursday, May 28, 2009 Sheet 5 of 33

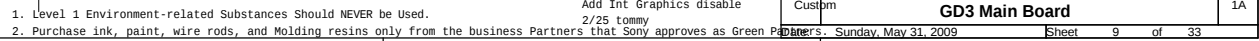
Print Date:	Thursday, May 20, 2009	Sheet	9	of	99
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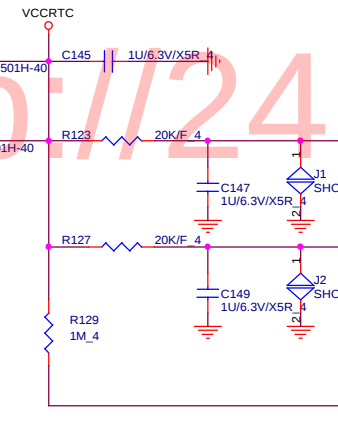
1. Level 1 Environment-related Substances Should NEVER be Used.

2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

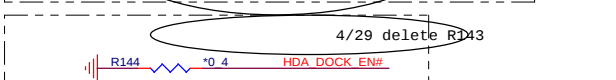
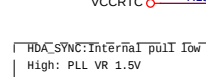
Print Date:	Thursday, May 20, 2009	Sheet	9	of	99
		1			







Pin connection diagram for the U4A module. The diagram shows connections between the U4A module and the LPC module. On the left, the U4A module has pins labeled RTC_XL_B13, RTC_XD13, C14, D17, A16, and A14. On the right, the LPC module has pins labeled FWH0 / LAD0, FWH1 / LAD1, FWH2 / LAD2, FWH3 / LAD3, FWH4 / LFRAME#, LDRQ0#, LDRQ1# / GPIO23, D33, B33, A32, C34, A34, E34, T71, T70, and AR0 IRO SERIRO. The connections are: RTC_XL_B13 to D33, RTC_XD13 to B33, C14 to A32, D17 to C34, A16 to A34, and A14 to E34. The T71 and T70 pins are connected to ground symbols.



No Reboot	
PCSPK	High = Enable Low = Disable (Default)

SPI_SDI	Intel Anti-Theft Technology
High = Enable Low = Disable (Default)	

(19) HDA_BCLK0

(19) HDA_SYNC0

(19) HDA_RST#0

(19) HDA_SDO0

C156

*27P 4

R152

33/F 4

HDA_BCLK R

R154

33/F 4

HDA_SYNC R

4/20 R598 delete

R157

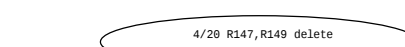
33/F 4

HDA_RST# R

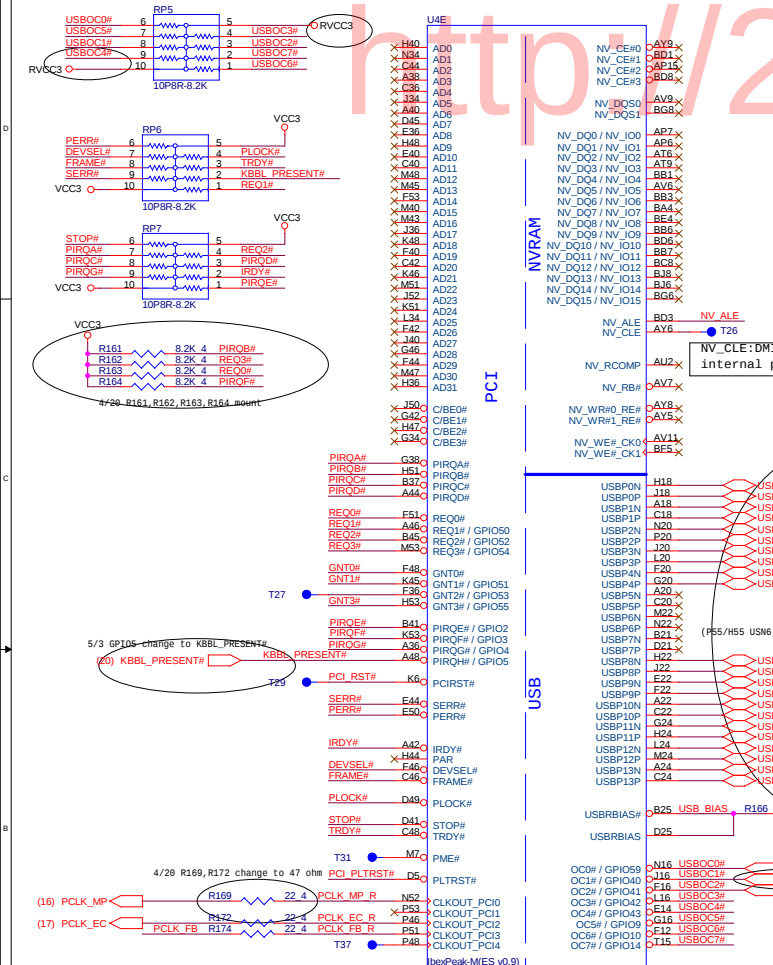
R159

33/F 4

HDA_SDO R



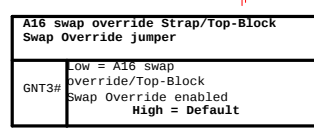
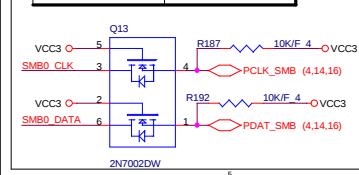
IBEX PEAK-M (PCI, USB, NVRAM)



GNT2#/GPIO53 :
High : Internal pull-up
Low : Configures DMI for ESI compatible operation (for servers only. Not for mobile/desktops)

SM BUS for C0 (CLOCK/DRAM)

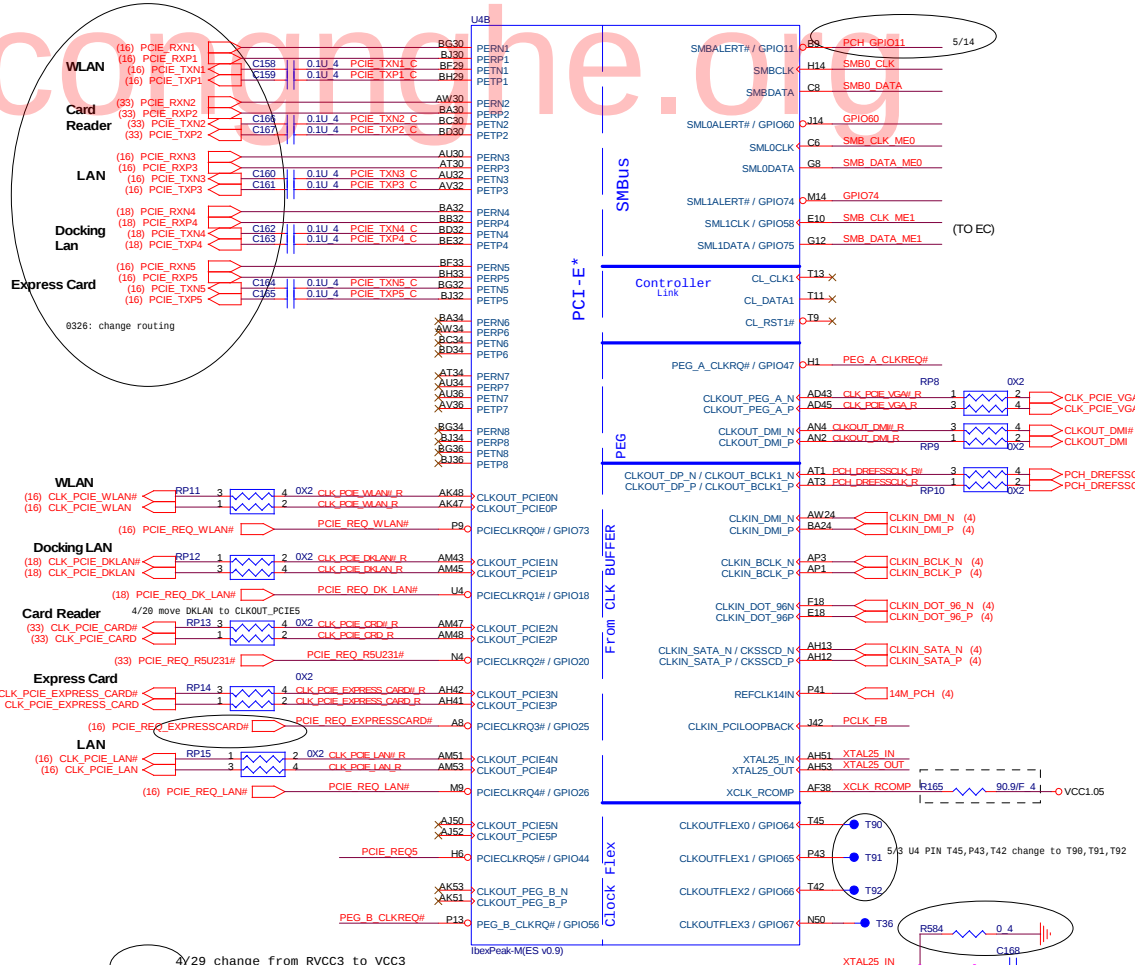
PCLK_SMB/PDAT_SMB	Address
SO-DIMM0	1010 000X A0h
SO-DIMM1	1010 010X A4h
ICS9LRS3105 Express Card	1101 0010 D2h



Anti-Theft Technology	
NV_ALE	High = Enable Low = Disable

Boot BIOS Strap		
GNT0#	GNT#1	Boot BIOS Location
0	0	* LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

IBEX PEAK-M (PCI-E, SMBUS, CLK)

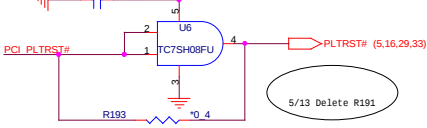


VCC3 4/29 change from RVCC3 to VCC3

VCC3 4/20 Change to VCC3

PCH GPIO11	R175	10K/F 4
GPIO60	R176	10K/F 4
SMB0 CLK	R177	2.2K/F 4
SMB0 DATA	R178	2.2K/F 4
SMB CLK ME0	R180	2.2K/F 4
SMB DATA ME0	R182	2.2K/F 4
GPIO74	R185	10K/F 4

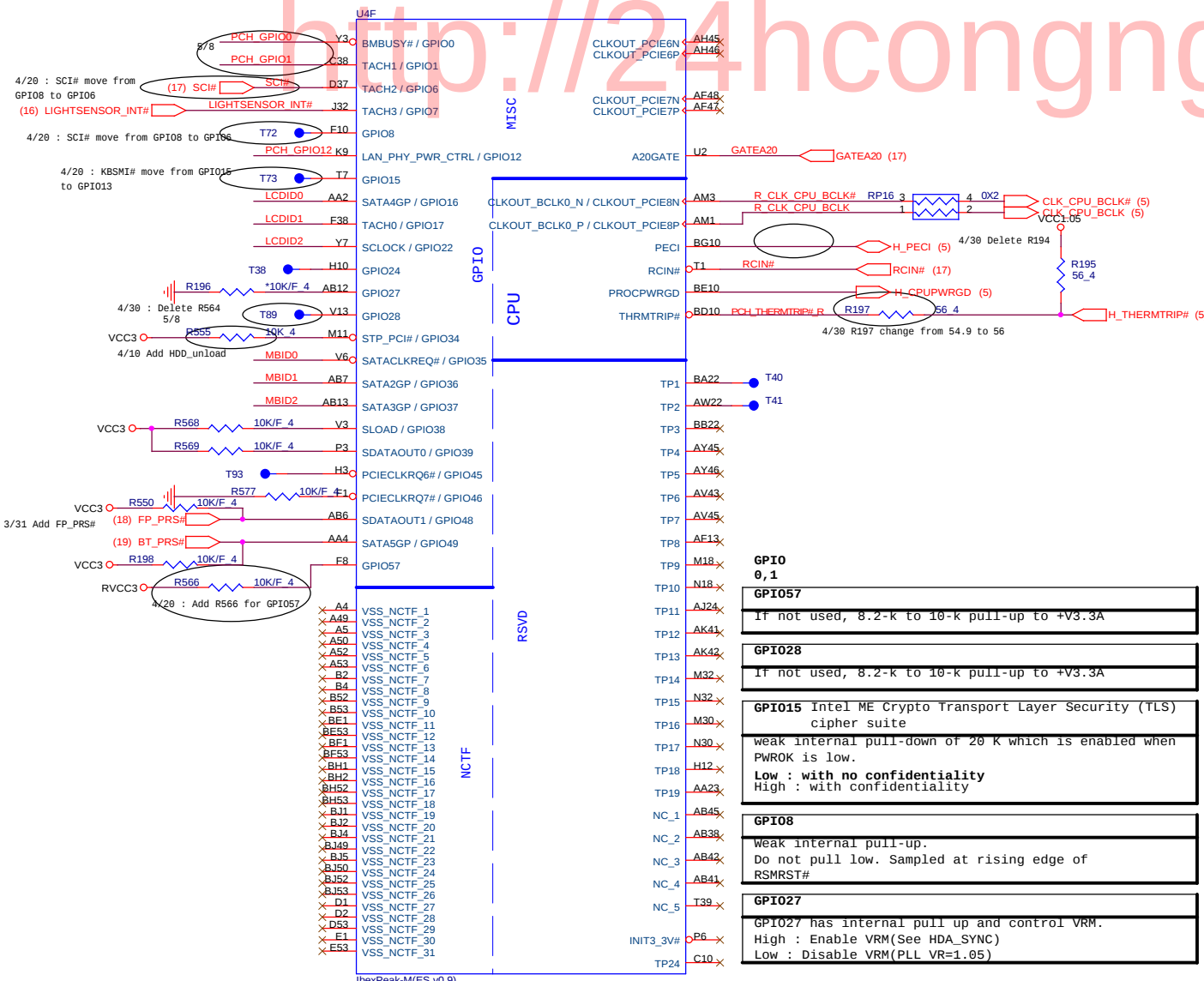
GPI056
If not used, 8.2-k Ω to 10-k Ω pull-up to +V3.3A



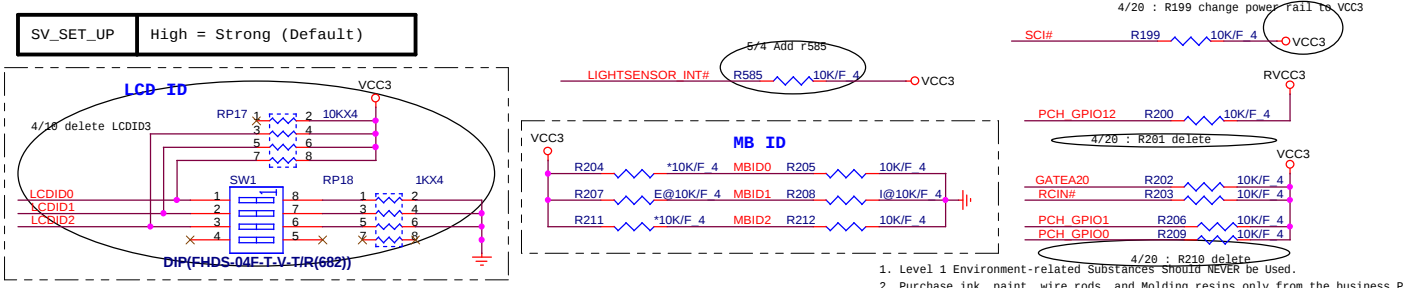
QUANTA
COMPUTER

Title		PCH 3/5(PCI)			
Size	Document Number				
Custom		GD3 Main Board			
Date:	Sunday, May 31, 2009	Sheet	11	of	33

IBEX PEAK-M (GPIO, VSS_NCTF, RSVD)



AY7	VSS[159]	VSS[259]	H49	VSS[0]	AK30
B11	VSS[160]	VSS[260]	H5	VSS[1]	VSS[80]
B15	VSS[161]	VSS[261]	J24	VSS[2]	VSS[81]
B19	VSS[162]	VSS[262]	AA20	VSS[3]	VSS[82]
B23	VSS[163]	VSS[263]	AA22	VSS[4]	VSS[83]
B31	VSS[164]	VSS[264]	K47	VSS[5]	VSS[84]
B35	VSS[165]	VSS[265]	AA26	VSS[6]	VSS[85]
B39	VSS[166]	VSS[266]	L14	VSS[7]	VSS[86]
B43	VSS[167]	VSS[267]	AA28	VSS[8]	VSS[87]
B47	VSS[168]	VSS[268]	L2	VSS[9]	VSS[88]
B7	VSS[169]	VSS[269]	AA30	VSS[10]	VSS[89]
BG12	VSS[170]	VSS[270]	AA32	VSS[11]	VSS[90]
B12	VSS[171]	VSS[271]	L38	VSS[12]	VSS[91]
B16	VSS[172]	VSS[272]	AA34	VSS[13]	VSS[92]
B20	VSS[173]	VSS[273]	AA36	VSS[14]	VSS[93]
B24	VSS[174]	VSS[274]	AA38	VSS[15]	VSS[94]
B30	VSS[175]	VSS[275]	AA40	VSS[16]	VSS[95]
B34	VSS[176]	VSS[276]	AA42	VSS[17]	VSS[96]
B38	VSS[177]	VSS[277]	AA44	VSS[18]	VSS[97]
B42	VSS[178]	VSS[278]	AA46	VSS[19]	VSS[98]
B46	VSS[179]	VSS[279]	AA48	VSS[20]	VSS[99]
B50	VSS[180]	VSS[280]	AA50	VSS[21]	VSS[100]
BC10	VSS[181]	VSS[281]	AA52	VSS[22]	VSS[101]
BC14	VSS[182]	VSS[282]	AA54	VSS[23]	VSS[102]
BC18	VSS[183]	VSS[283]	AA56	VSS[24]	VSS[103]
BC22	VSS[184]	VSS[284]	AA58	VSS[25]	VSS[104]
BC26	VSS[185]	VSS[285]	AA60	VSS[26]	VSS[105]
BC30	VSS[186]	VSS[286]	AA62	VSS[27]	VSS[106]
BC34	VSS[187]	VSS[287]	AA64	VSS[28]	VSS[107]
BC38	VSS[188]	VSS[288]	AA66	VSS[29]	VSS[108]
BC42	VSS[189]	VSS[289]	AA68	VSS[30]	VSS[109]
BC46	VSS[190]	VSS[290]	AA70	VSS[31]	VSS[110]
BC50	VSS[191]	VSS[291]	AA72	VSS[32]	VSS[111]
BD48	VSS[192]	VSS[292]	AA74	VSS[33]	VSS[112]
BD49	VSS[193]	VSS[293]	AA76	VSS[34]	VSS[113]
BD50	VSS[194]	VSS[294]	AA78	VSS[35]	VSS[114]
BE12	VSS[195]	VSS[295]	AA80	VSS[36]	VSS[115]
BE16	VSS[196]	VSS[296]	AA82	VSS[37]	VSS[116]
BE20	VSS[197]	VSS[297]	AA84	VSS[38]	VSS[117]
BE24	VSS[198]	VSS[298]	AA86	VSS[39]	VSS[118]
BE30	VSS[199]	VSS[299]	AA88	VSS[40]	VSS[119]
BE34	VSS[200]	VSS[300]	AA90	VSS[41]	VSS[120]
BE38	VSS[201]	VSS[301]	AA92	VSS[42]	VSS[121]
BE42	VSS[202]	VSS[302]	AA94	VSS[43]	VSS[122]
BE46	VSS[203]	VSS[303]	AA96	VSS[44]	VSS[123]
BE50	VSS[204]	VSS[304]	AA98	VSS[45]	VSS[124]
BE54	VSS[205]	VSS[305]	AA100	VSS[46]	VSS[125]
BE58	VSS[206]	VSS[306]	AA102	VSS[47]	VSS[126]
BE62	VSS[207]	VSS[307]	AA104	VSS[48]	VSS[127]
BE66	VSS[208]	VSS[308]	AA106	VSS[49]	VSS[128]
BE70	VSS[209]	VSS[309]	AA108	VSS[50]	VSS[129]
BE74	VSS[210]	VSS[310]	AA110	VSS[51]	VSS[130]
BE78	VSS[211]	VSS[311]	AA112	VSS[52]	VSS[131]
BE82	VSS[212]	VSS[312]	AA114	VSS[53]	VSS[132]
BE86	VSS[213]	VSS[313]	AA116	VSS[54]	VSS[133]
BE90	VSS[214]	VSS[314]	AA118	VSS[55]	VSS[134]
BE94	VSS[215]	VSS[315]	AA120	VSS[56]	VSS[135]
BE98	VSS[216]	VSS[316]	AA122	VSS[57]	VSS[136]
BE102	VSS[217]	VSS[317]	AA124	VSS[58]	VSS[137]
BE106	VSS[218]	VSS[318]	AA126	VSS[59]	VSS[138]
BE110	VSS[219]	VSS[319]	AA128	VSS[60]	VSS[139]
BE114	VSS[220]	VSS[320]	AA130	VSS[61]	VSS[140]
BE118	VSS[221]	VSS[321]	AA132	VSS[62]	VSS[141]
BE122	VSS[222]	VSS[322]	AA134	VSS[63]	VSS[142]
BE126	VSS[223]	VSS[323]	AA136	VSS[64]	VSS[143]
BE130	VSS[224]	VSS[324]	AA138	VSS[65]	VSS[144]
BE134	VSS[225]	VSS[325]	AA140	VSS[66]	VSS[145]
BE138	VSS[226]	VSS[326]	AA142	VSS[67]	VSS[146]
BE142	VSS[227]	VSS[327]	AA144	VSS[68]	VSS[147]
BE146	VSS[228]	VSS[328]	AA146	VSS[69]	VSS[148]
BE150	VSS[229]	VSS[329]	AA148	VSS[70]	VSS[149]
BE154	VSS[230]	VSS[330]	AA150	VSS[71]	VSS[150]
BE158	VSS[231]	VSS[331]	AA152	VSS[72]	VSS[151]
BE162	VSS[232]	VSS[332]	AA154	VSS[73]	VSS[152]
BE166	VSS[233]	VSS[333]	AA156	VSS[74]	VSS[153]
BE170	VSS[234]	VSS[334]	AA158	VSS[75]	VSS[154]
BE174	VSS[235]	VSS[335]	AA160	VSS[76]	VSS[155]
BE178	VSS[236]	VSS[336]	AA162	VSS[77]	VSS[156]
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BE186	VSS[238]	VSS[338]	AA166	VSS[79]	VSS[158]
BE190	VSS[239]	VSS[339]	AA168	VSS[80]	VSS[159]
BE194	VSS[240]	VSS[340]	AA170	VSS[81]	VSS[160]
BE198	VSS[241]	VSS[341]	AA172	VSS[82]	VSS[161]
BE202	VSS[242]	VSS[342]	AA174	VSS[83]	VSS[162]
BE206	VSS[243]	VSS[343]	AA176	VSS[84]	VSS[163]
BE210	VSS[244]	VSS[344]	AA178	VSS[85]	VSS[164]
BE214	VSS[245]	VSS[345]	AA180	VSS[86]	VSS[165]
BE218	VSS[246]	VSS[346]	AA182	VSS[87]	VSS[166]
BE222	VSS[247]	VSS[347]	AA184	VSS[88]	VSS[167]
BE226	VSS[248]	VSS[348]	AA186	VSS[89]	VSS[168]
BE230	VSS[249]	VSS[349]	AA188	VSS[90]	VSS[169]
BE234	VSS[250]	VSS[350]	AA190	VSS[91]	VSS[170]
BE238	VSS[251]	VSS[351]	AA192	VSS[92]	VSS[171]
BE242	VSS[252]	VSS[352]	AA194	VSS[93]	VSS[172]
BE246	VSS[253]	VSS[353]	AA196	VSS[94]	VSS[173]
BE250	VSS[254]	VSS[354]	AA198	VSS[95]	VSS[174]
BE254	VSS[255]	VSS[355]	AA200	VSS[96]	VSS[175]
BE258	VSS[256]	VSS[356]	AA202	VSS[97]	VSS[176]
BE262	VSS[257]	VSS[357]	AA204	VSS[98]	VSS[177]
BE266	VSS[258]	VSS[358]	AA206	VSS[99]	VSS[178]
BE270	VSS[259]	VSS[359]	AA208	VSS[100]	VSS[179]



QUANTA COMPUTER

GD3 Main Board

Rev 1A

Size Custom

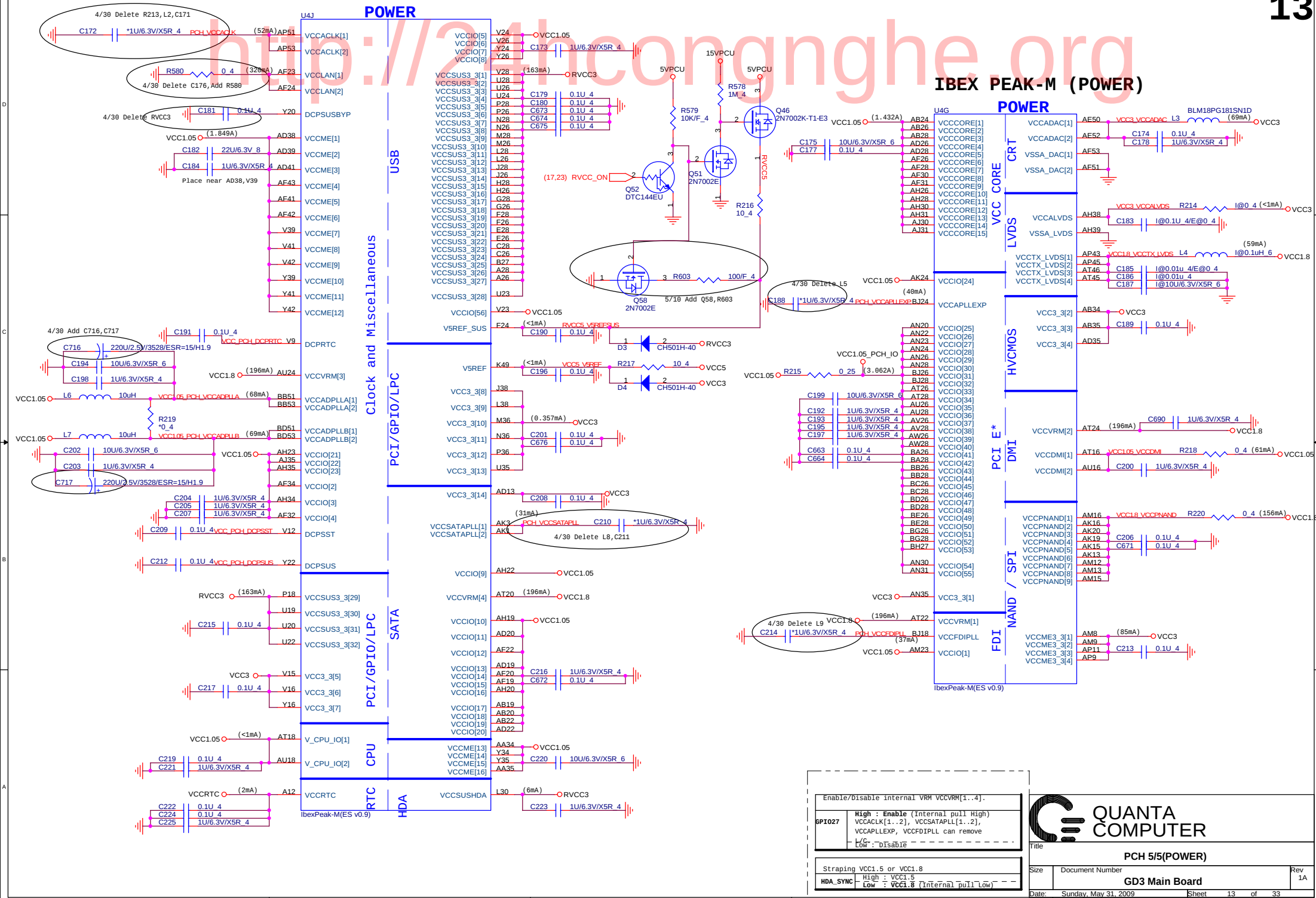
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Date: Sunday, May 31, 2009

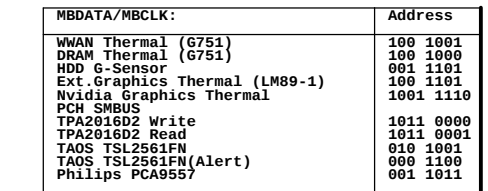
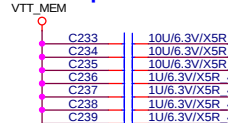
Sheet 12 of 33

1. Level 1 Environment-related Substances should NEVER be used.
 2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

IBEX PEAK-M (POWER)



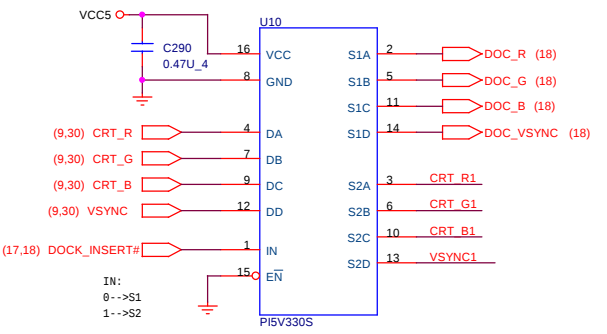
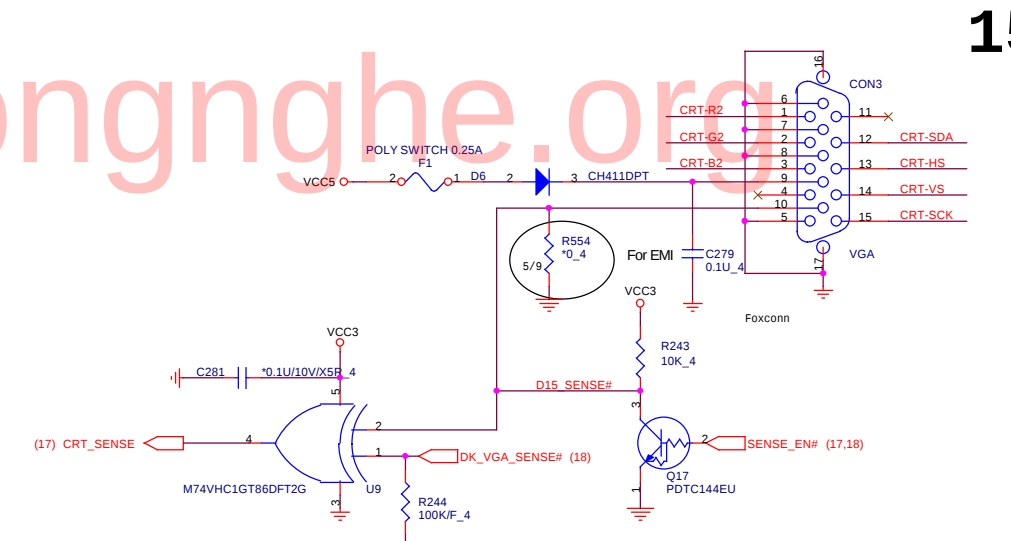
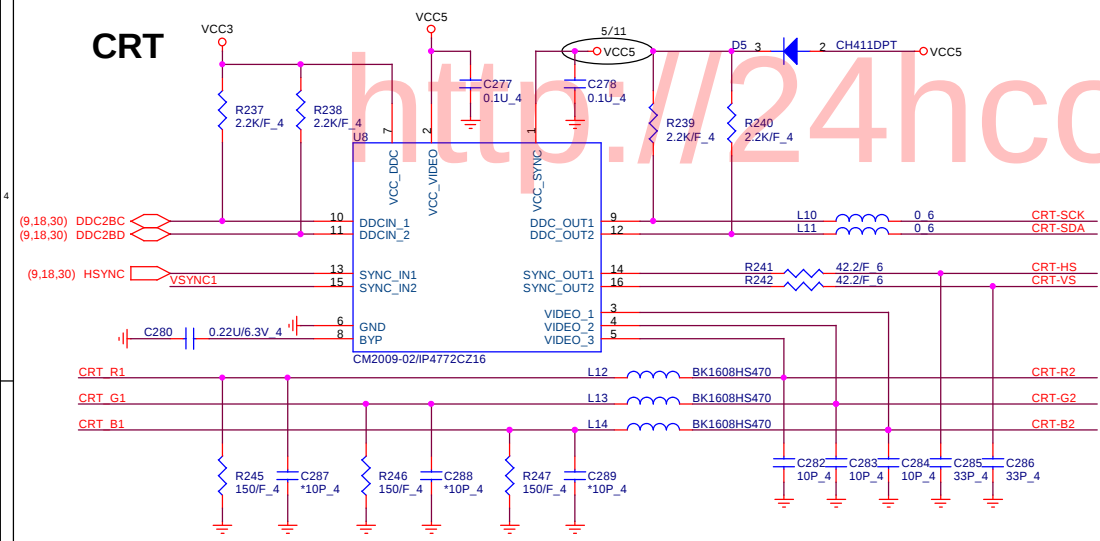
Place these Caps near So-Dimm0.



Date:	Thursday, May 20, 2009	Sheet	14	of	33
		1			

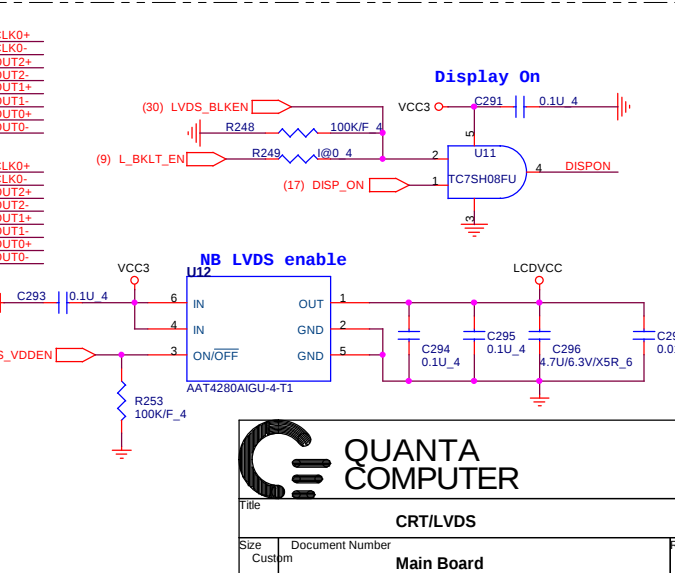
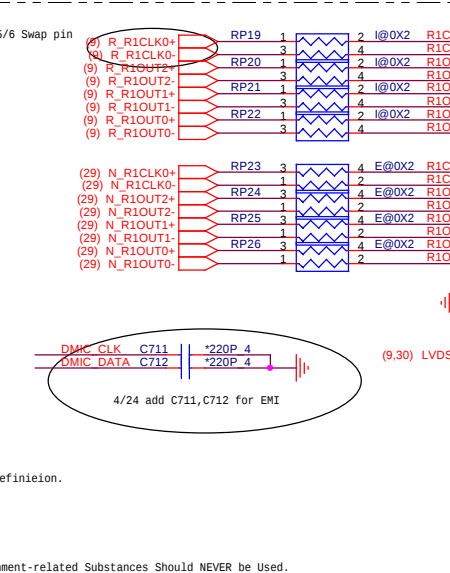
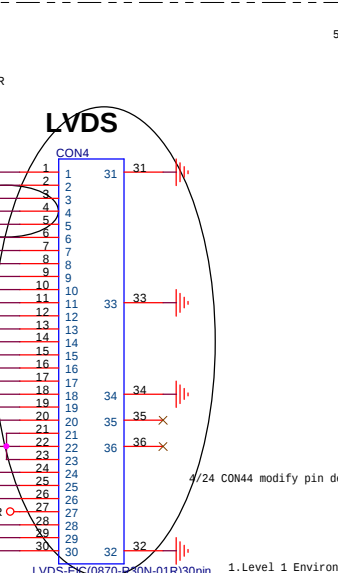
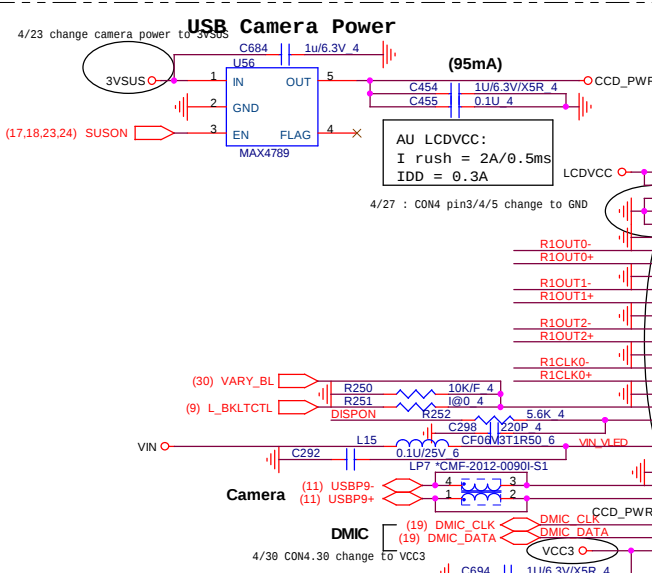
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partner.

CRT



[EC Behavior]
 CRT_SENSE: H=>L or L=>H detect which one is inserting
 Docking_Insert#: H=>L detect which one is inserting
 [CRT SW]
 When Docking_Insert# is low switching RGB to Docking CRT
 [SENSE_EN#]
 Input for CRT pin10, EC assert to high when CRT
 insert[NEO Request]

	SENSE_EN#	DK_VGA_SENSE#	D15_SENSE#	Docking_Insert#	CRT_SENSE
Without Docking					
MB CRT no Plug	L	L	H	H	L
MB CRT Plug in	L	L	H => L	H	L => H
MB CRT plug out	L	L	L => H	H	H => L
With Docking					
All no Plug	L	L	H	L	L
MB CRT Plug in	L	L	H => L	L	L => H
MB CRT plug out	L	L	L => H	L	H => L
DOCK DVI Plug in	L	H => L	H	L	H => L
DOCK DVI Plug out	L	L => H	H	L	L => H
DOCK CRT Plug	L	H => L	H	L	L => H
DOCK CRT/DVI Plug	L	L => H	H	L	L => H



LAN Board BTB

CON38

WLAN PCIE

(11) PCIE_TXP1
(11) PCIE_TXN1
(11) PCIE_RXP1
(11) PCIE_RXN1

LAN PCIE

(11) PCIE_TXP3
(11) PCIE_TXN3
(11) PCIE_RXP3
(11) PCIE_RXN3

Express Card

(11) PCIE_RXN5
(11) PCIE_RXP5
(11) PCIE_TXN5
(11) PCIE_TXP5

WWAN USB

USBP10+ (11)
USBP10- (11)

WIMAX USB

USBP12+ (11)
USBP12- (11)

USB PORT0

USBP0+ (11)
USBP0- (11)

USB PORT1

USBP1+ (11)
USBP1- (11)

Express Card

USBP3+ (11)
USBP3- (11)

Express Card

USBP3+ (11)
USBP3- (11)

Express Card

USBP3+ (11)
USBP3- (11)

Express Card

USBP3+ (11)
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USBP3- (11)

Express Card

USBP3+ (11)
USBP3- (11)

Express Card

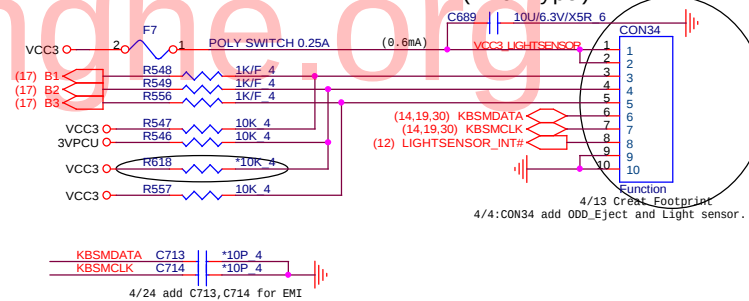
USBP3+ (11)
USBP3- (11)

Express Card

USBP3+ (11)
USBP3- (11)

M/B to Function Board

(FFC Type)



Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

Debug Port

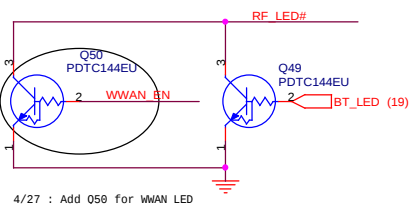
Debug Port

Debug Port

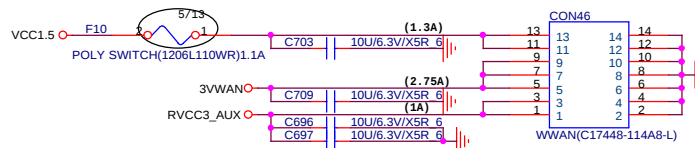
Debug Port

Debug Port

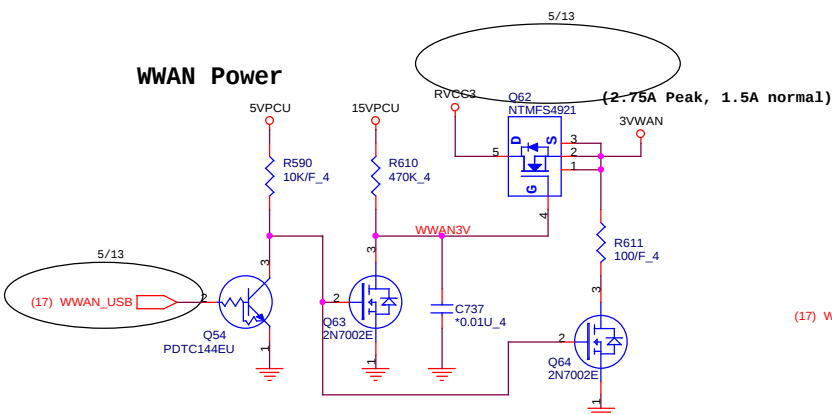
Debug Port



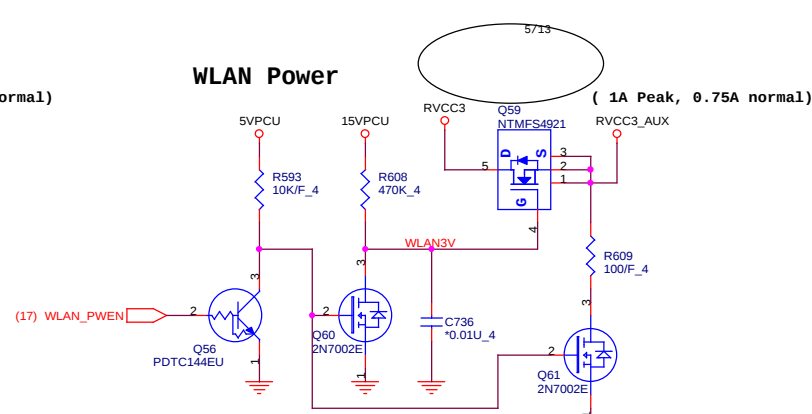
4/27 : Add Q50 for WWAN LED



WWAN Power

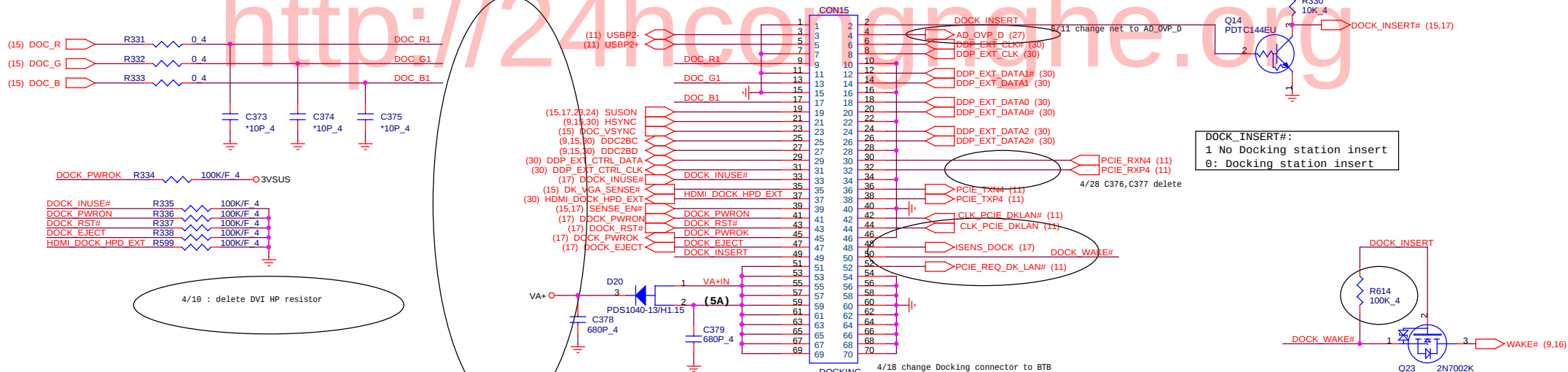


WLAN Power

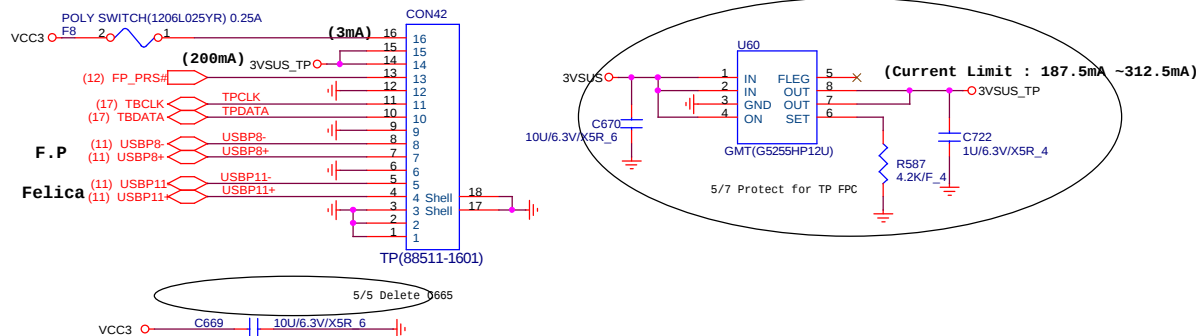


Title				LAN Board/ Function Board				Rev 1A
Size	Document Number							
Main Board								
Date:	Monday, June 01, 2009				Sheet	16	of	33

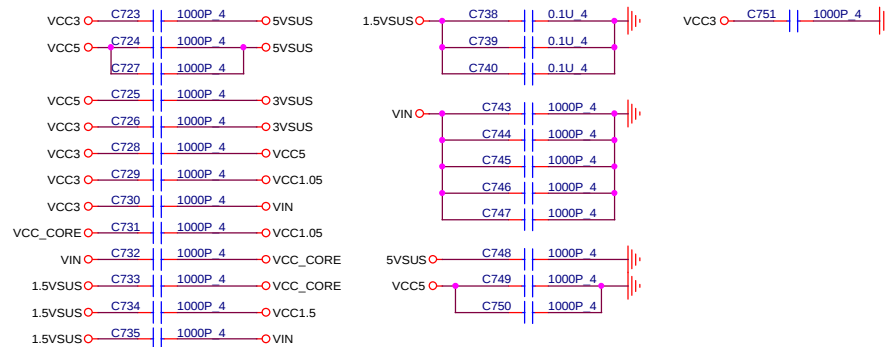
Docking



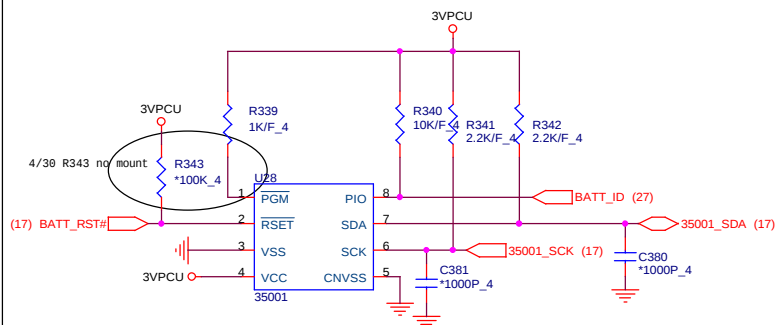
T/P to M/B (FPC)



EMI

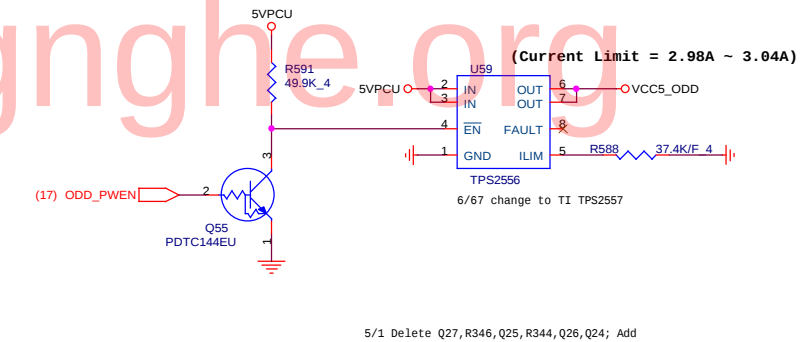
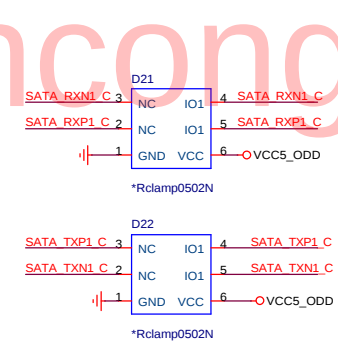
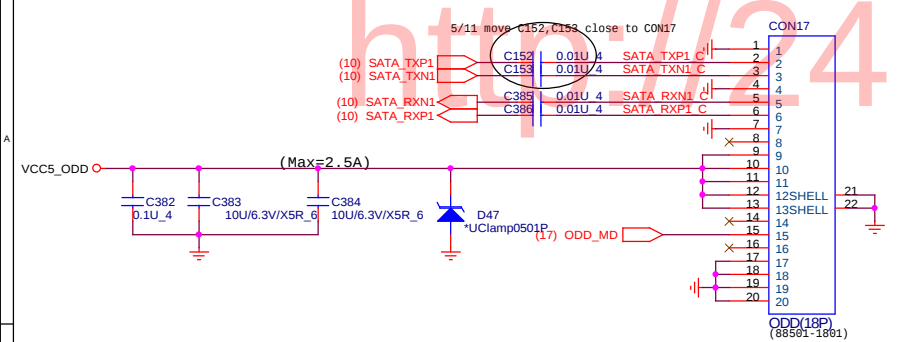


35001

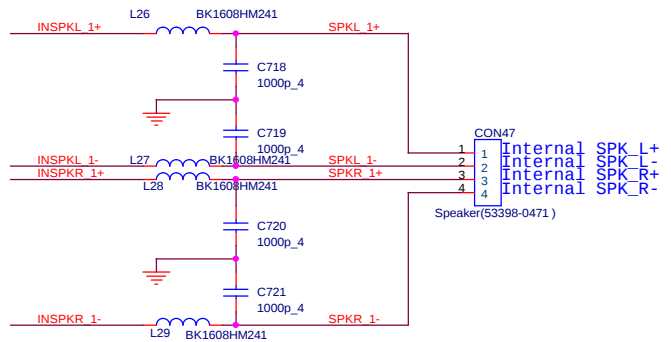


Title				Docking/TPM/35001					
Size		Document Number						Rev	
		Main Board						1A	
Date		Monday, June 01, 2009			Sheet		18 of 33		

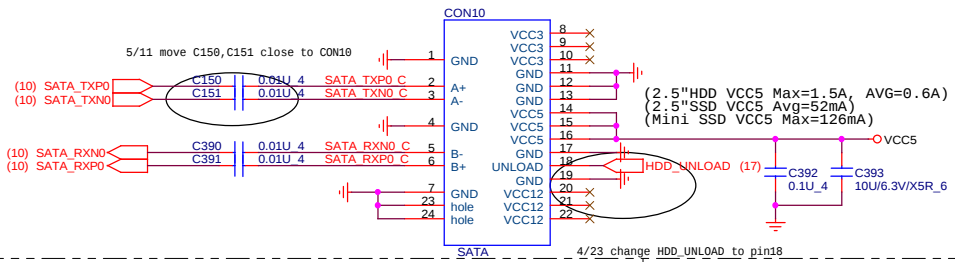
ODD CONNECTOR (FPC 18 pin)



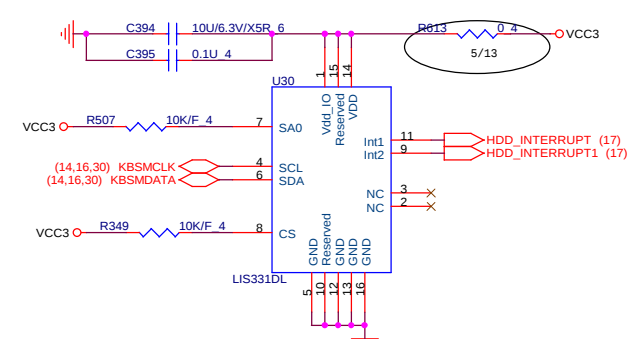
SPEAKER CONNECTOR



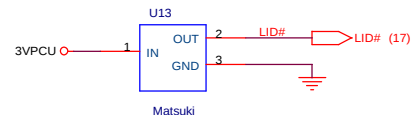
HDD CONNECTOR



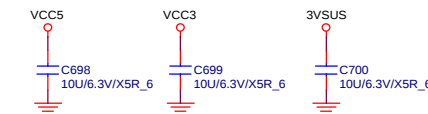
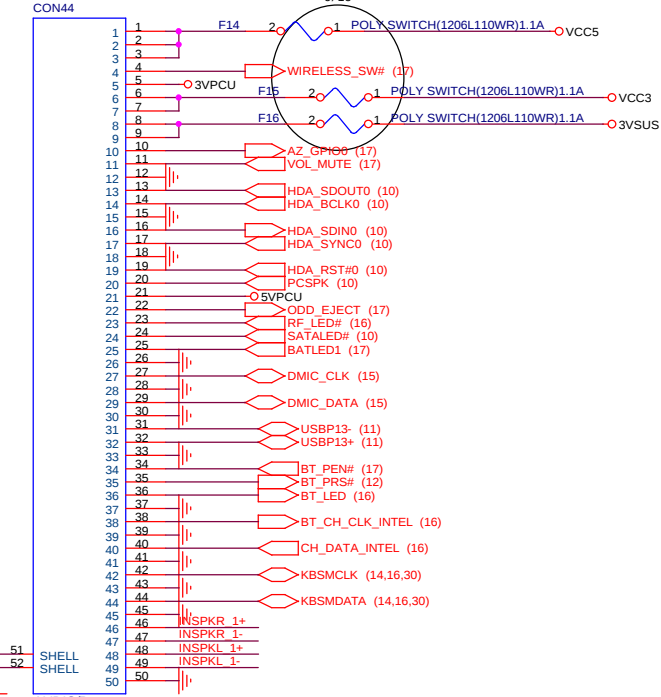
HDD PROTECT



Magnetic Lid Switch



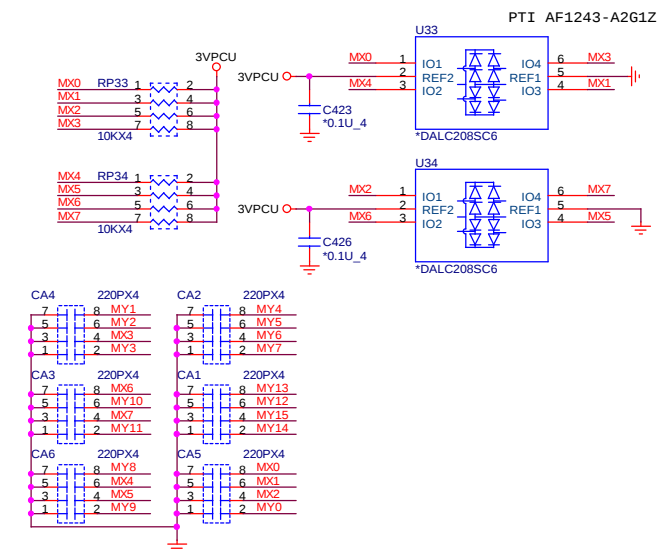
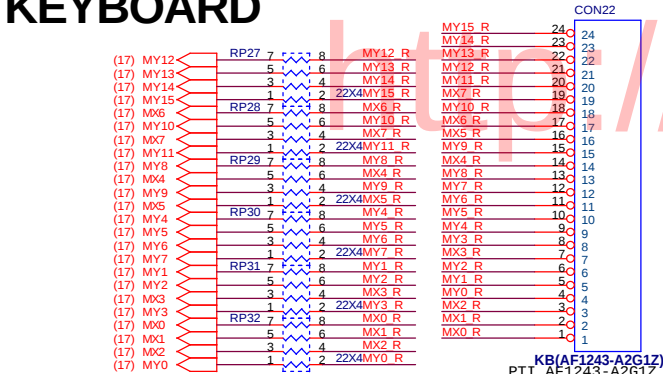
Audio Board



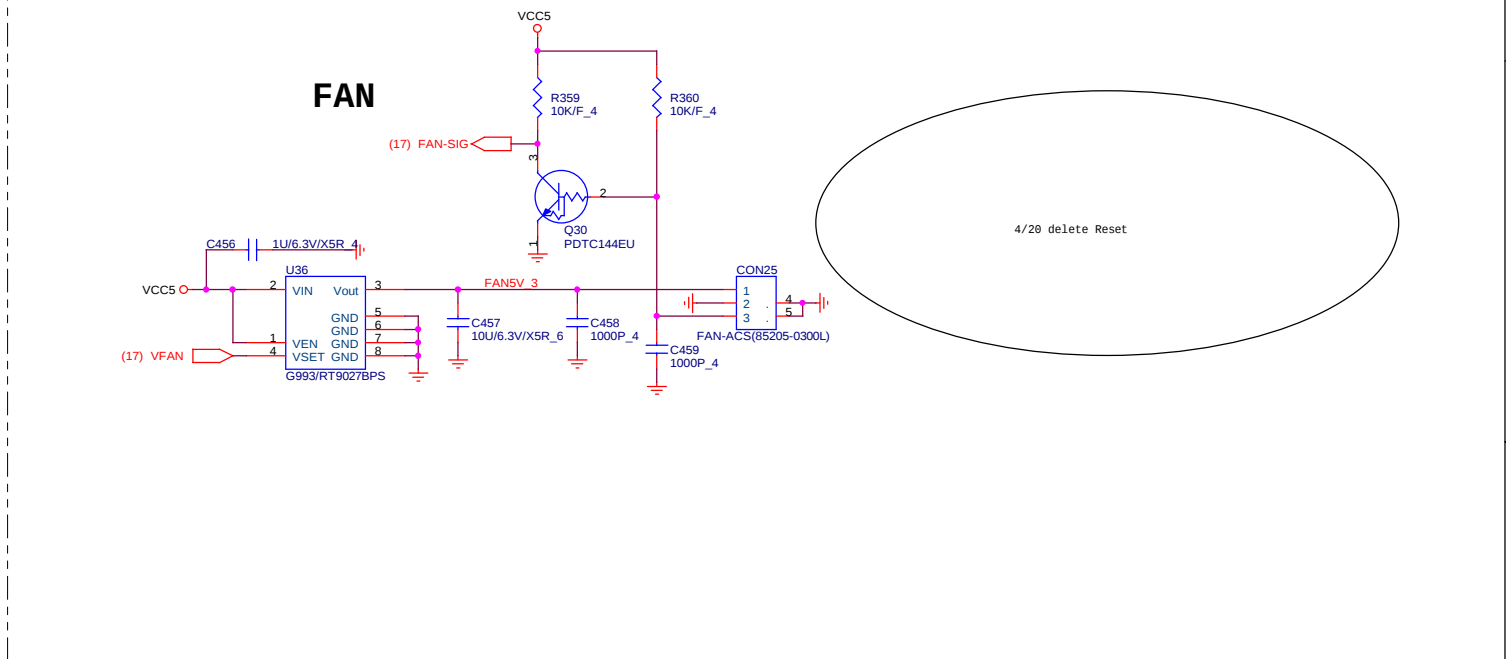
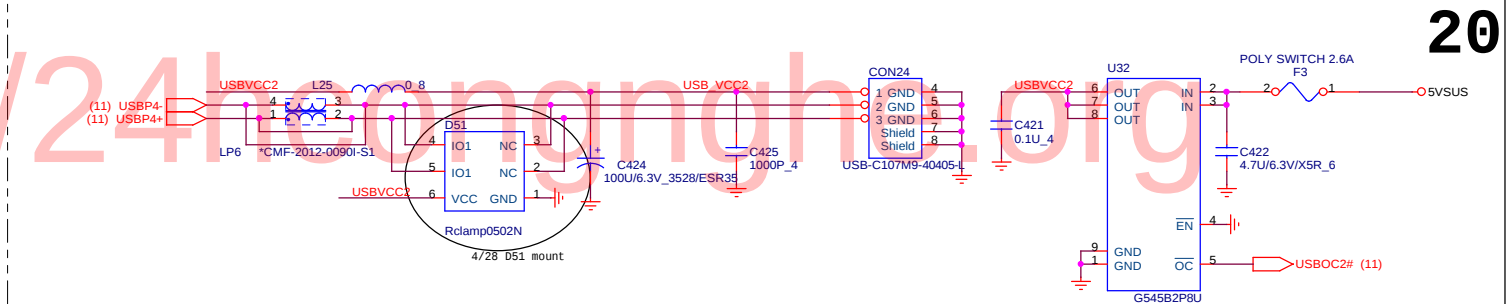
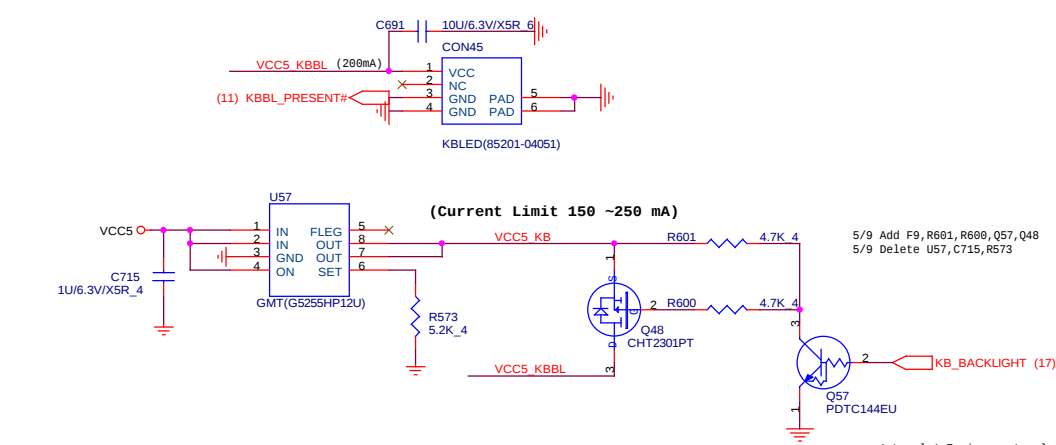
Title: HDD/ODD/SSD		
Size	Document Number	Rev 1A
Main Board		
Date: Monday, June 01, 2009	Sheet 19 of 33	

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

KEYBOARD



KB BACKLIGHT



QUANTA COMPUTER

KB/USB/FAN

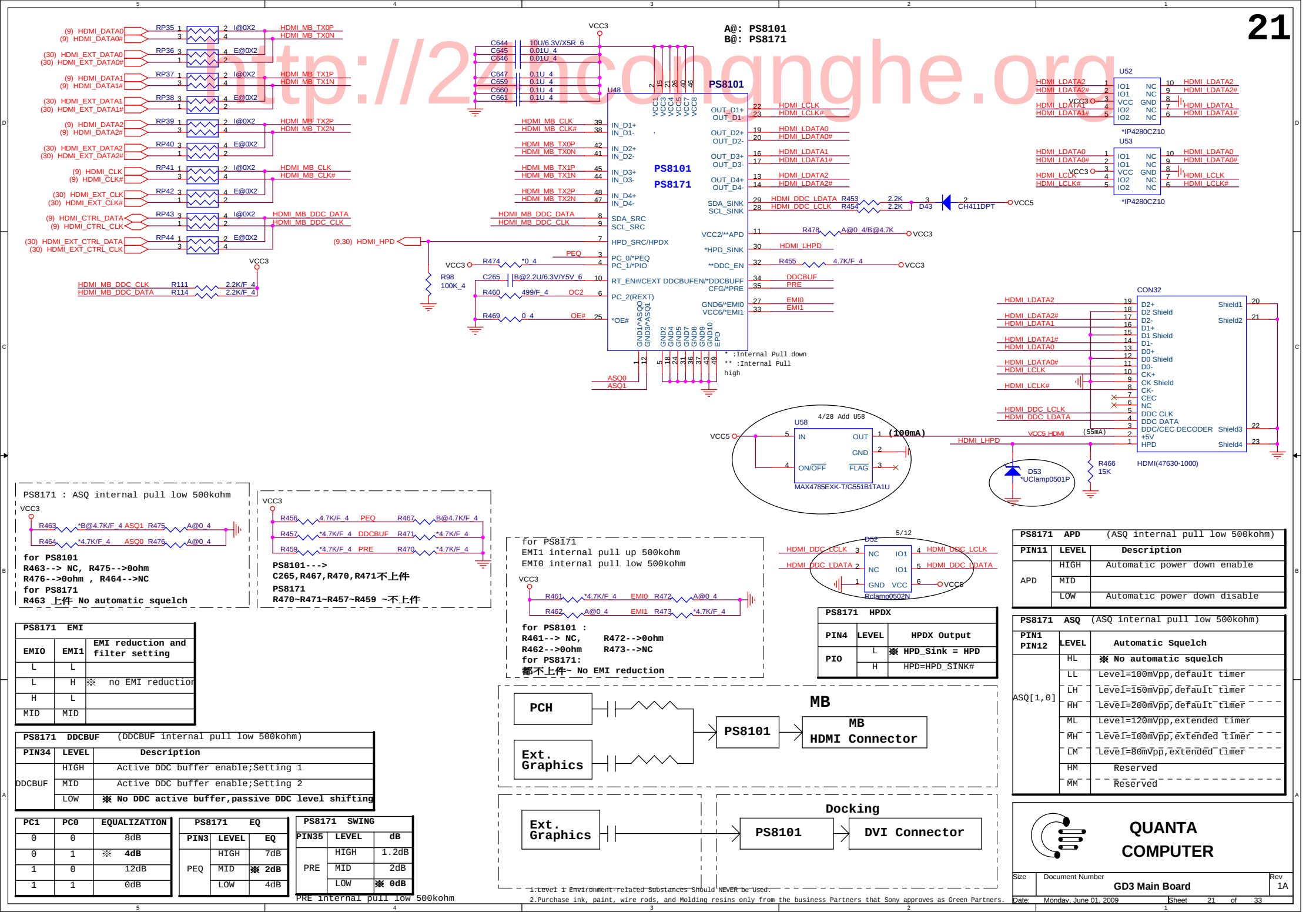
Rev 1A

1.Level 1 Environment-related Substances Should NEVER be Used.

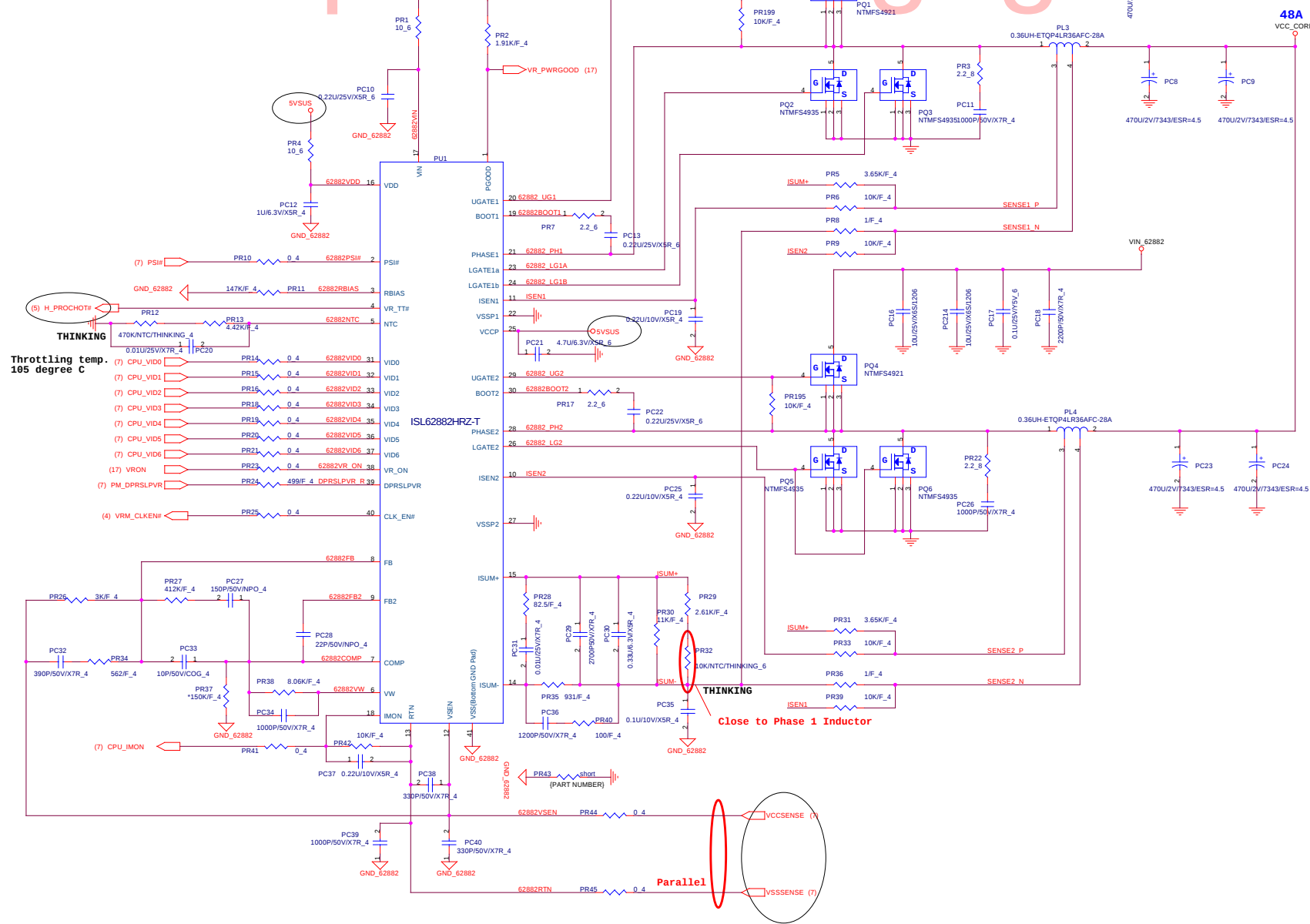
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

Date: Monday, June 01, 2009

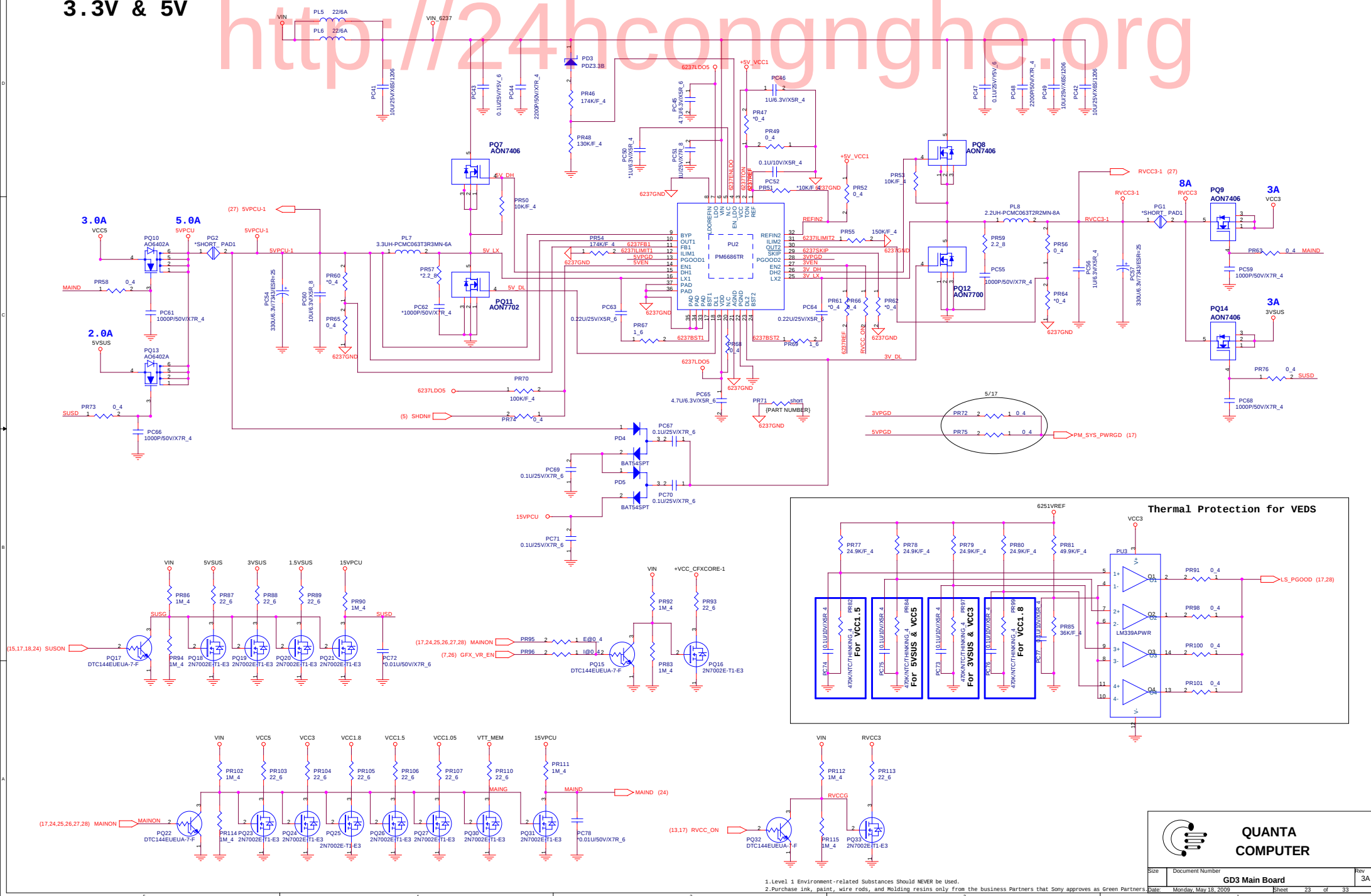
Sheet 20 of 33



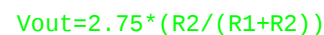
http://24hcongnghie.org



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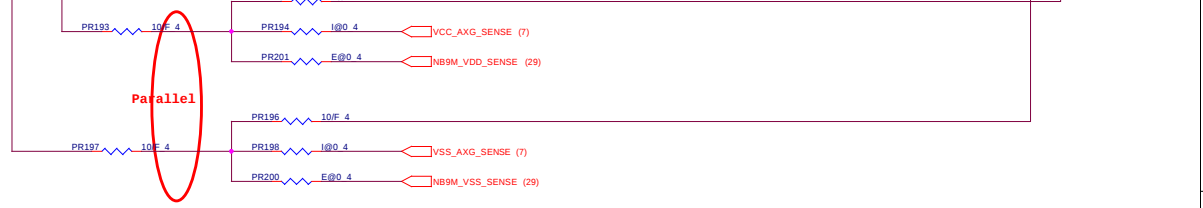
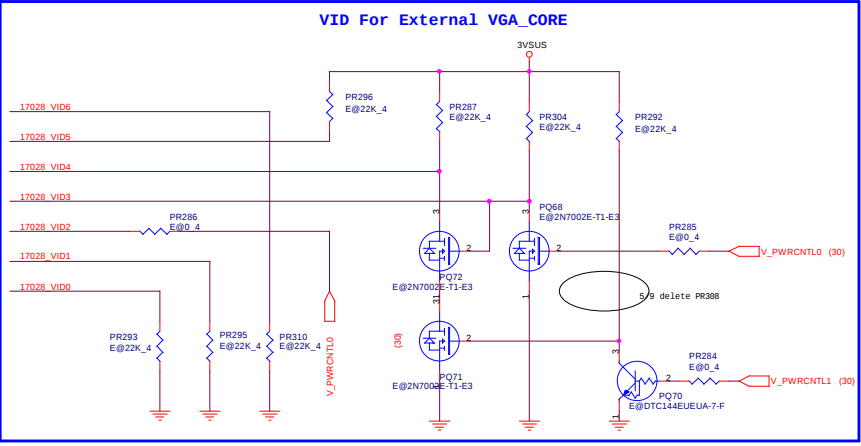
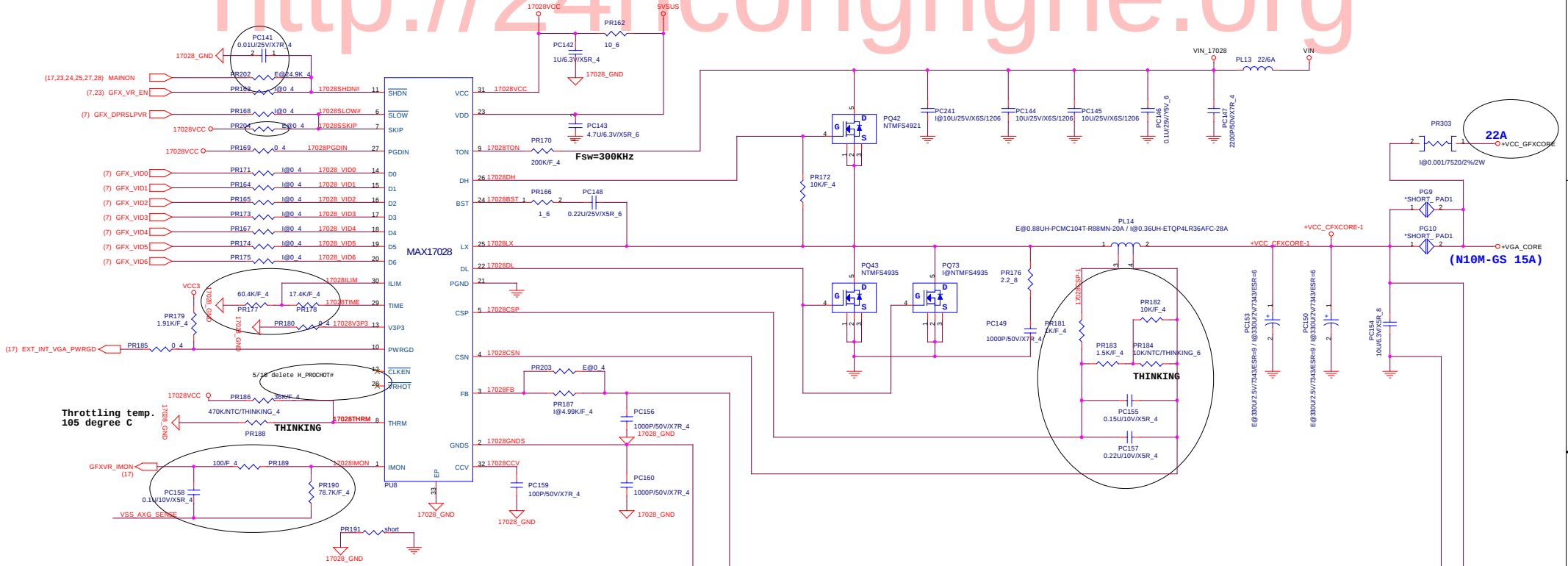


<http://24hcongnghe.org>



VTT_SEL	VCC1.05-1
High	1.05V
Low	1.10V

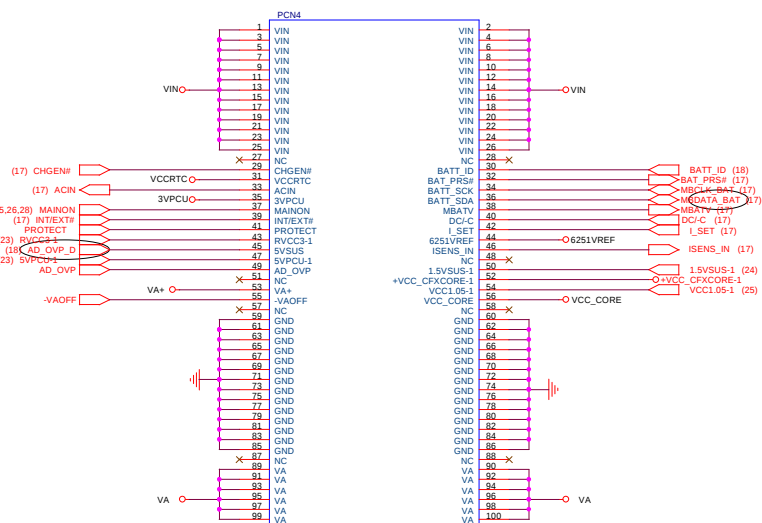
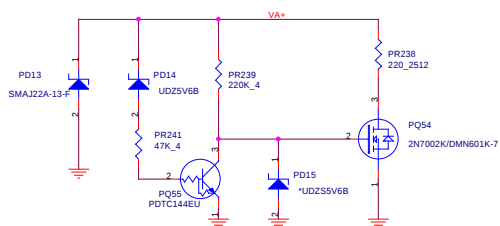
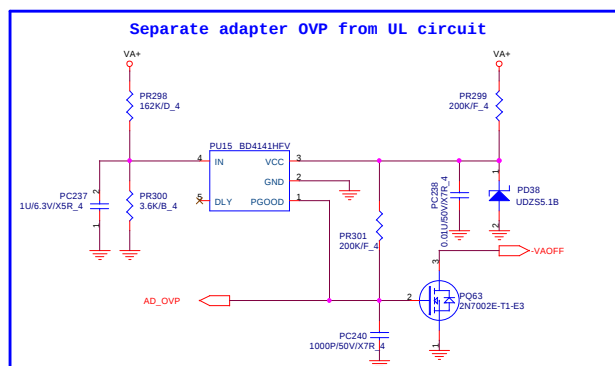
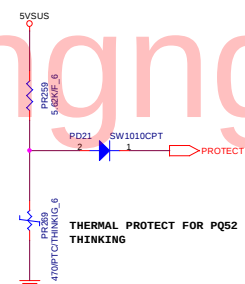
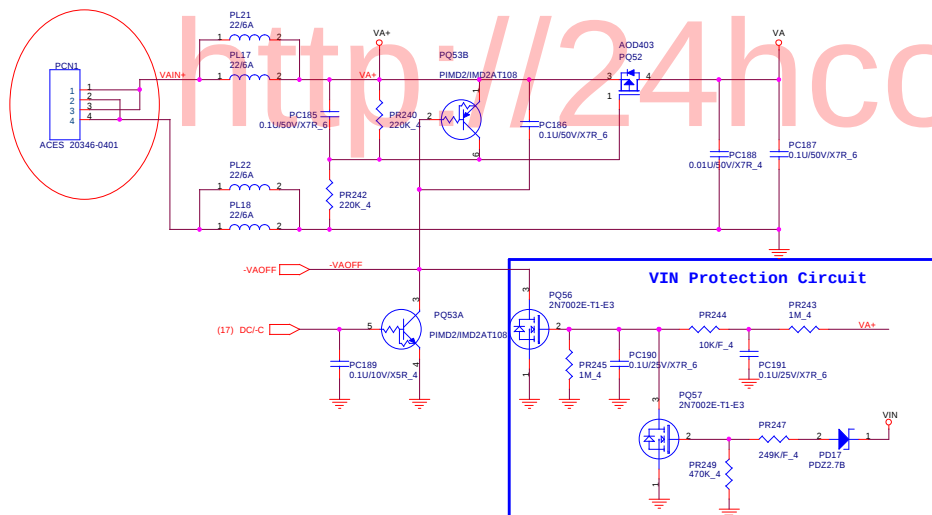
http://24hcongnghe.org



External VGA_CORE voltage setting:

V_PWRCNTL1	V_PWRCNTL0	VGA_VID	VGA_CORE
0	0	0101000	1.0V
0	1	0110100	0.85V
1	0	0111000	0.8V

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Quanta P/N

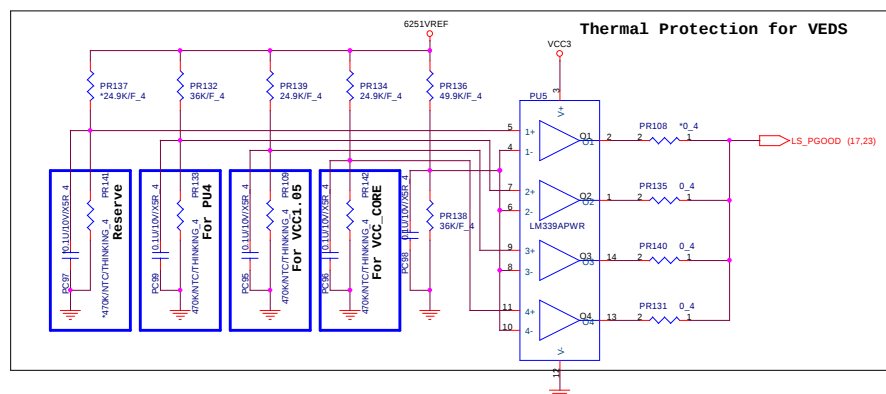
4/18 change pin defined

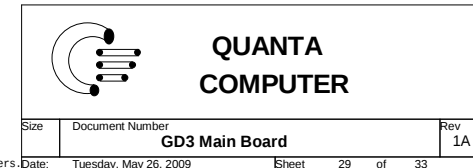


**QUANTA
COMPUTER**

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Size	Document Number	Rev
	GD3 MB	1A
Date	Friday, May 15, 2009	Sheet 27 of 33





2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

nVidia VGA_CORE voltage setting:

V_PWRCNTL1	V_PWRCNTL0	VGA_VID	VGA_CORE
0	0	0101000	1.0V
0	1	0110100	0.85V
1	0	0111000	0.8V

Logical Strap Bit Mapping

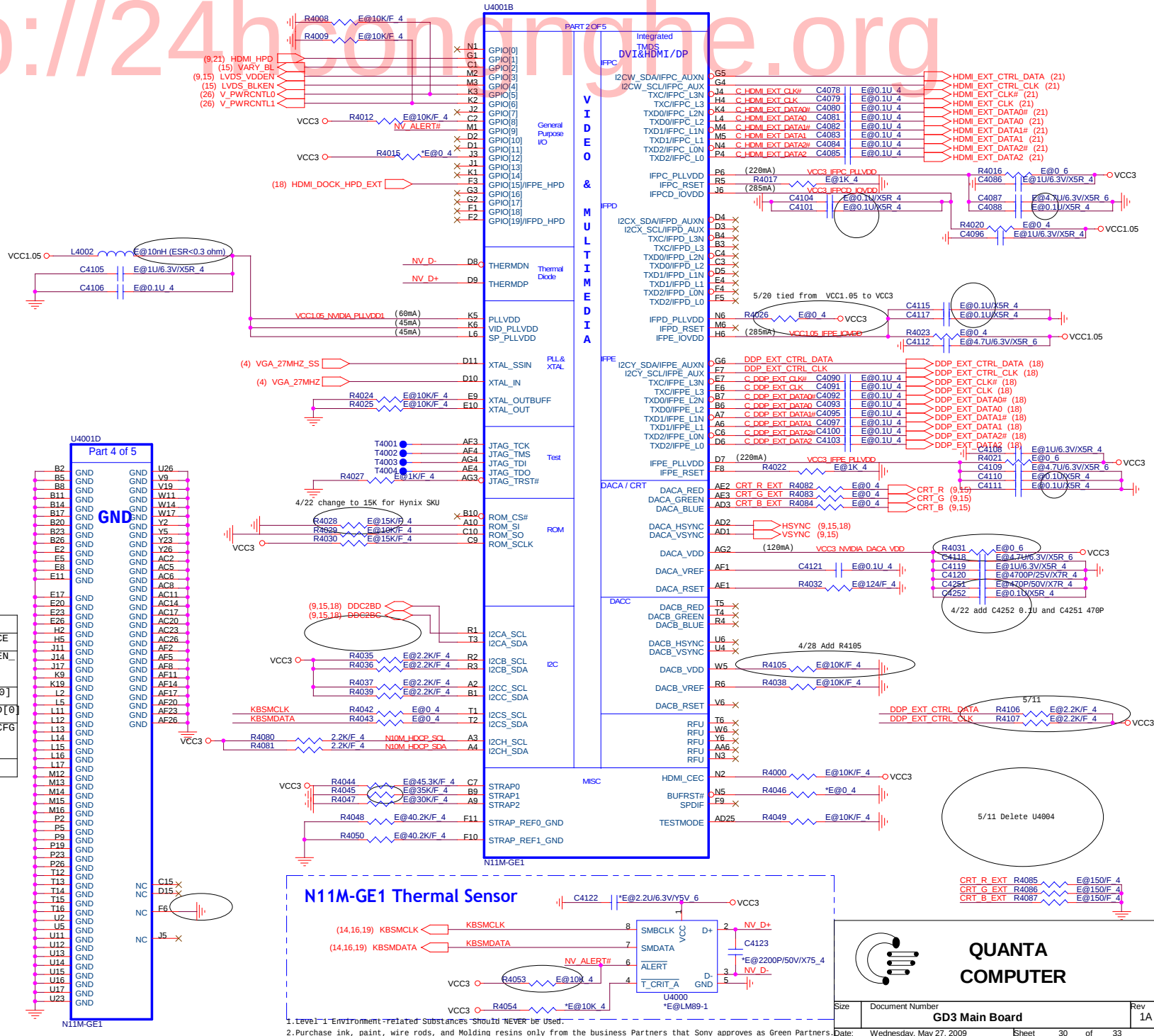
Resister Value	Pull to VDD	Pull to GND
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111

Strap Bit Define

Straps	Bit 3	Bit 2	Bit 1	Bit 0
ROM_S0	XCLK_417	FB_0_BAR_SIZESMB_ALT_ADDR	VGA_DEVICE	
ROM_SCLK	PCI_DEVID[4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP1	3GIO_PADCFG6	3GIO_PADCFG6	3GIO_PADCFG6	3GIO_PADCFG6
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]

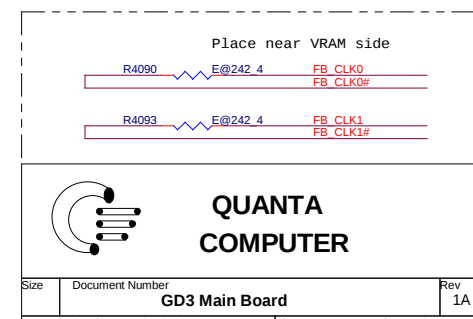
VRAM Strap:ROM_SI ; RAMCFG[x]

VRAM Capacity	VRAM Vender	ID	R4028
DDR3 64Mx16	Samsung	0011	PD20K
	Hynix	0010	PD15K



TOP

BOT



BOT

Place near U4008

Size	Document Number	Rev
	GD3 Main Board	1A
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